

1 **DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION**

2 As the below named inventor, I hereby declare that:

3 My residence, post office address and citizenship are as stated
4 below next to my name.

5 I believe I am the original, first and joint inventor of the subject
6 matter which is claimed and for which a patent is sought on the
7 invention entitled: Methods of Forming Contacts, Methods of Contacting
8 Lines, Methods of Forming Dynamic Random Access Memory Circuitry,
9 Methods of Operating Integrated Circuitry, Integrated Circuits, Dynamic
10 Random Access Memory Circuitry, the specification of which is attached
11 hereto.

12 I hereby state that I have reviewed and understand the contents
13 of the above-identified specification, including the claims.

14 I acknowledge the duty to disclose information known to me to
15 be material to patentability as defined in Title 37, Code of Federal
16 Regulations §1.56.

17 **PRIOR FOREIGN APPLICATIONS:**

18 I hereby state that no applications for foreign patents or inventor's
19 certificates have been filed prior to the date of execution of this
20 declaration.

21 I hereby declare that all statements made herein of my own
22 knowledge are true and that all statements made on information and
23 belief are believed to be true; and further that these statements were
24 made with the knowledge that willful false statements and the like so

made are punishable by fine or imprisonment, or both, under
Section 1001 of Title 18 of the United States Code and that such willful
false statement may jeopardize the validity of the application or any
patent issued therefrom.

* * * * *

Full name of inventor: Robert Kerr

Inventor's Signature: 

Date: 8-26-98

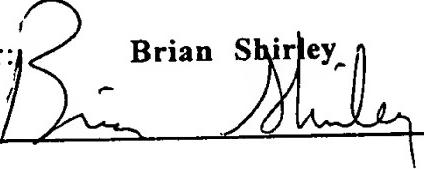
Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 3089 E. Bonview Drive
Boise, ID 83712

* * * * *

Full name of inventor: Brian Shirley

Inventor's Signature: 

Date: 8/31/98

Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 2484 Sunshine Drive
Boise, ID 83712

DRAFTS & DRAWINGS

* * * * *

1 Full name of inventor: **Luan C. Tran**

2 Inventor's Signature: _____

3 Date: _____

4 Residence: **Meridian, Idaho**

5 Citizenship: **U.S.A.**

6 Post Office Address: **1125 W. Sandy Ct.
Meridian, ID 83642**

* * * * *

7 Full name of inventor: **Tyler A. Lowrey**

8 Inventor's Signature: _____

9 Date: _____

10 Residence: **Boise, Idaho**

11 Citizenship: **U.S.A.**

12 Post Office Address: **2599 E. Plateau Drive
Boise, ID 83712**

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DEPARTMENT OF COMMERCE

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5 * * * * *

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7 Inventor's Signature: _____

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12 * * * * *

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14 Inventor's Signature: _____

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17 Citizenship: **U.S.A.**

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Boise, ID 83712

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* * * * *

1 Full name of inventor: **Luan C. Tran**

2 Inventor's Signature: _____

3 Date: _____

4 Residence: **Meridian, Idaho**

5 Citizenship: **U.S.A.**

6 Post Office Address: **1125 W. Sandy Ct.
Meridian, ID 83642**

* * * * *

7 Full name of inventor: **Tyler A. Lowrey**

8 Inventor's Signature: Tyler A. Lowrey

9 Date: 8/27/78

10 Residence: **Boise, Idaho**

11 Citizenship: **U.S.A.**

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Boise, ID 83712**

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8 Lines, Methods of Operating Integrated Circuitry, and Integrated Circuits,
9 Serial No. 09/146,115, filed September 2, 1998.

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false statement may jeopardize the validity of the application or any patent issued therefrom.

* * * * *

Full name of inventor: Luan C. Tran

Inventor's Signature: John C. Flan

Date: 1-18-1999

Residence: Meridian, Idaho

Citizenship: U.S.A.

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Digitized by srujanika@gmail.com